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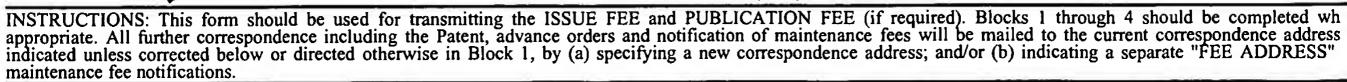
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	APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
•	10/053.543	01/24/2002	Masahiro Shimizu	57454-329	3990

TITLE OF INVENTION: INTEGRATED CIRCUIT HAVING A MEMORY CELL TRANSISTOR WITH A GATE OXIDE LAYER WHICH IS THICKER THÂN THE GATE OXIDE LAYER OF A PERIPHERAL CIRCUIT TRANSISTOR

APPLN. TYPE	SMALL ENTITY	ISSUE FE	E FEE PUBLICATION FEE		TOTAL FEE(S) DU	E DATE DUE
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EXAMINER PERALTA, GINETTE		ART UNIT 2814		CLASS-SUBCLASS		
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